

EAST - (10892810 gate insul with deuterium.wsp:1)

FileViewEditToolsWindowHelp

Drafts

BBS:

BBS:

BBS:

BBS: knall

BBS: 19 21 28

BBS: 32 with 33

Pending

Active

L1: (11) 10/802810

L2: (12593) deuterium

L3: (934053) gate

L4: (1815983) insulat34 dielectric

L5: (108862) L3 near 2 L4

L6: (56) L5 near 9 L2

L7: (58) 6 and 2

L8: (1022656) (sidewall side ad) (wall surface)

L9: (35) 2 with 8

L10: (12) 6 and 9

Failed

Saved

S2: (1) 09/048877

S3: (1786) "KOHINKUJIKE PHILIPS".as.

S1: (0) 10/759914

S4: (0) 10/748908

S5: (1) 10/799810

US-PAAC (10892810) DOCUMENT: 04-10

Doc: 04-10

6 and 9

April 2005

	U	I	Author	Document	Issu	P	Title	Curred	Current	I	Retrie	S	C	P	Image	Doc	P
1			Moull, Chan	US 200400	2004	12	SOI device having increased reliability	438/29	257/E21.21							US 20040	
2			Lyding, Jose	US 200902	2003	11	DEUTERIUM TREATMENT OF SEMICONDUCTOR	438/30	257/E21.19							US 20089	
3			Watanabe, US	200500	2005	18	Semiconductor device with an insulat	257/41								US 20050	
4			Hosotani, K	US 200500	2005	9	Magnetic random access memory and	438/3								US 20050	
5			Kunikiyo, T	US 200201	2002	31	Method of manufacturing semiconduc	438/30	257/E21.19							US 200201	
6			Kunikiyo, T	US 200200	2002	5	Semiconductor device and SOI substra	257/41	257/E21.19							US 20020	
7			Lyding, Jose	US 200200	2002	11	Deuterium treatment of semiconducto	438/78	257/410;							US 20020	
8			Lyding, Jos	US 683330	2004	10	Deuterium treatment of semiconducto	438/30	438/795;							US 68333	
9			Kunikiyo, T	US 666106	2003	5	Semiconductor device and SOI substra	257/41	257/347;							US 666106	
10			Kunikiyo, T	US 650072	2002	3	Method of manufacturing semiconduc	438/30	257/E21.19							US 650072	

S. No. 10 of 10 (100.00%)

Ready

2.0M